

ABSTRACT OF THE DISCLOSURE

A first signal line and a second signal line are paired, and in one signal line selection period, CPU of the inspection circuit controls a write circuit and writes analog signals into the first signal line selected by means of a switch of the selection circuit.

In the next signal line selection period, CPU controls a read circuit and reads output signals from the second signal line selected by means of the switch. CPU detects a short circuit between the paired signal lines based upon the output signals from the second signal line.